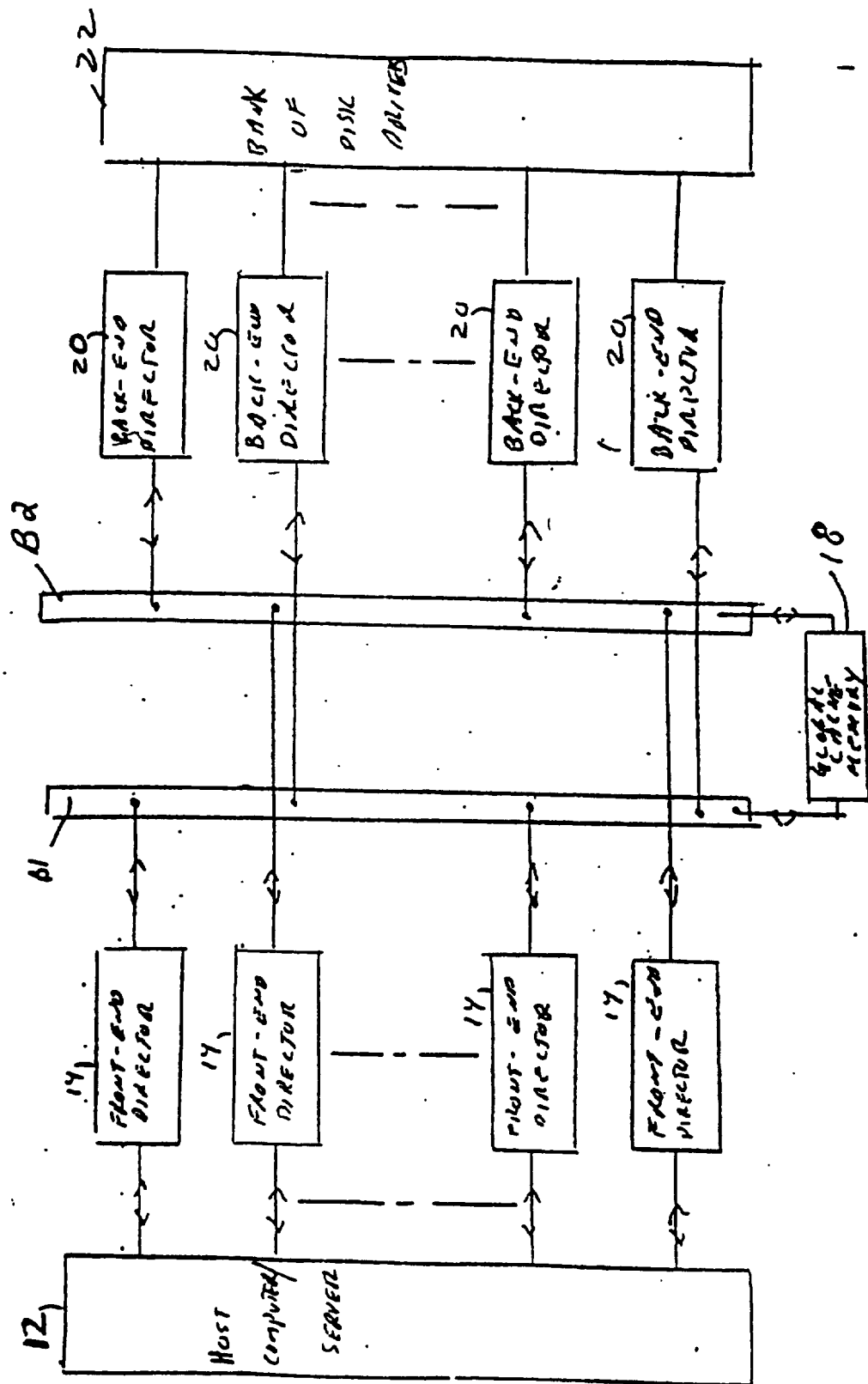
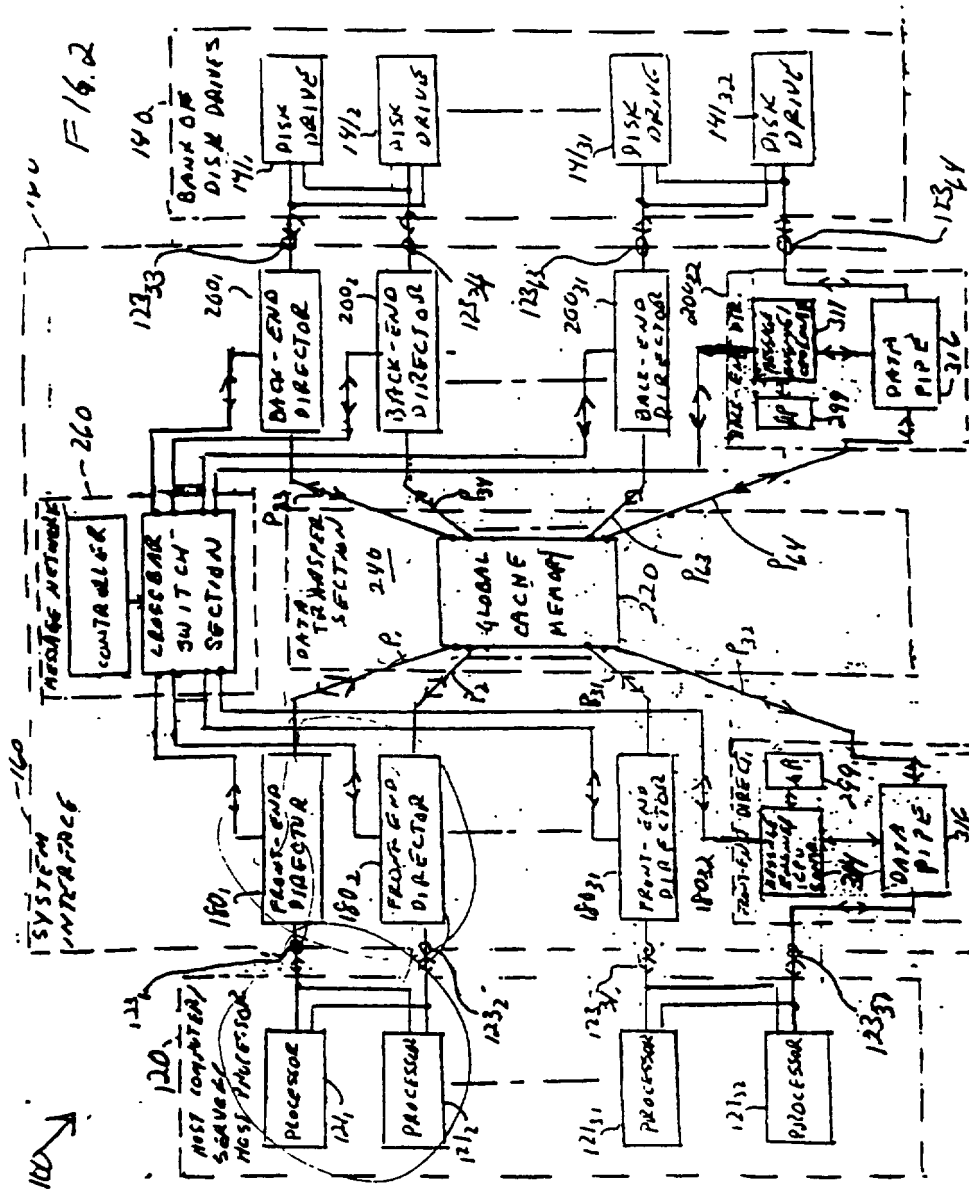


SCANNED, # 12

FIG. 1  
 SANES



SCANNED, # 12



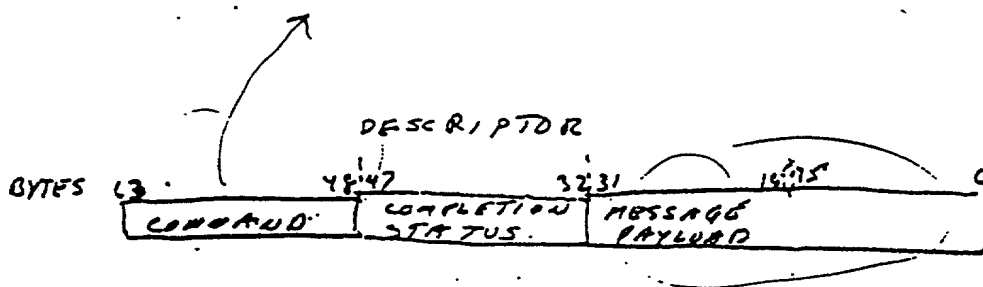


FIG 2A

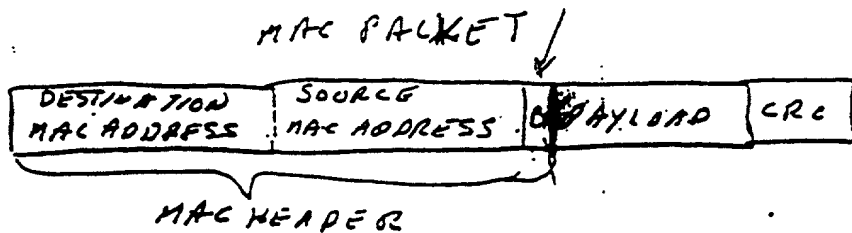
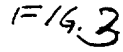
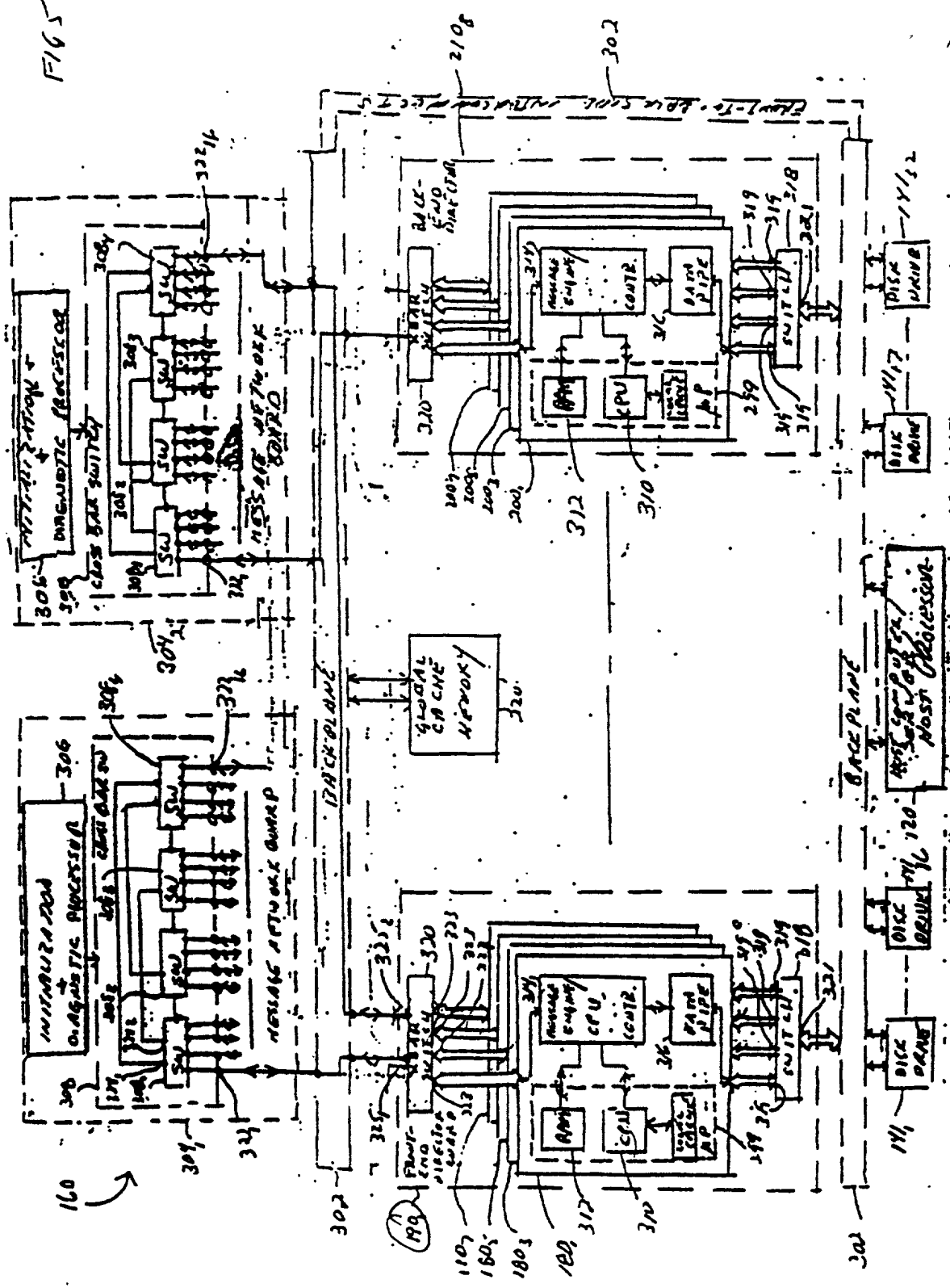


FIG 2B





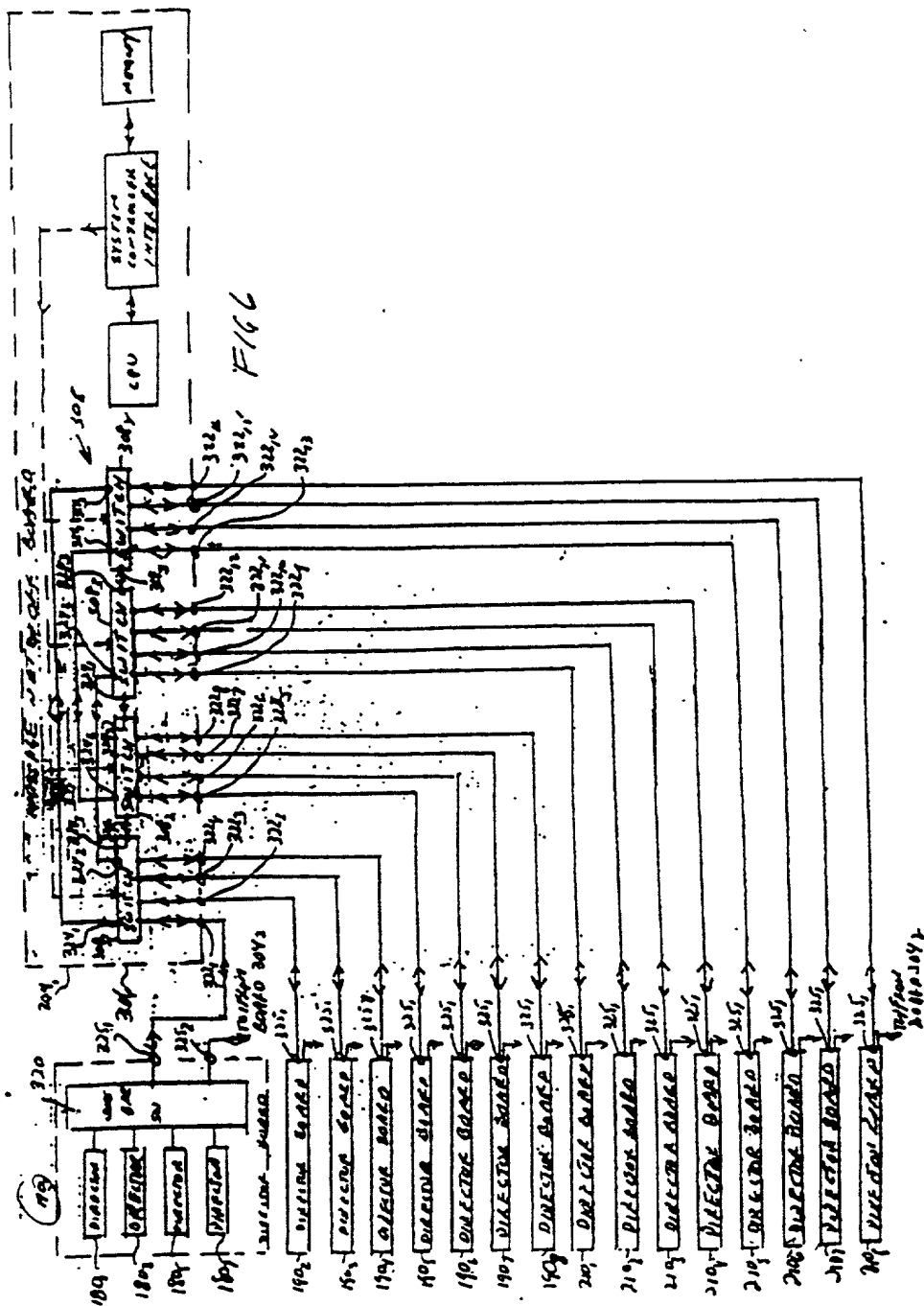
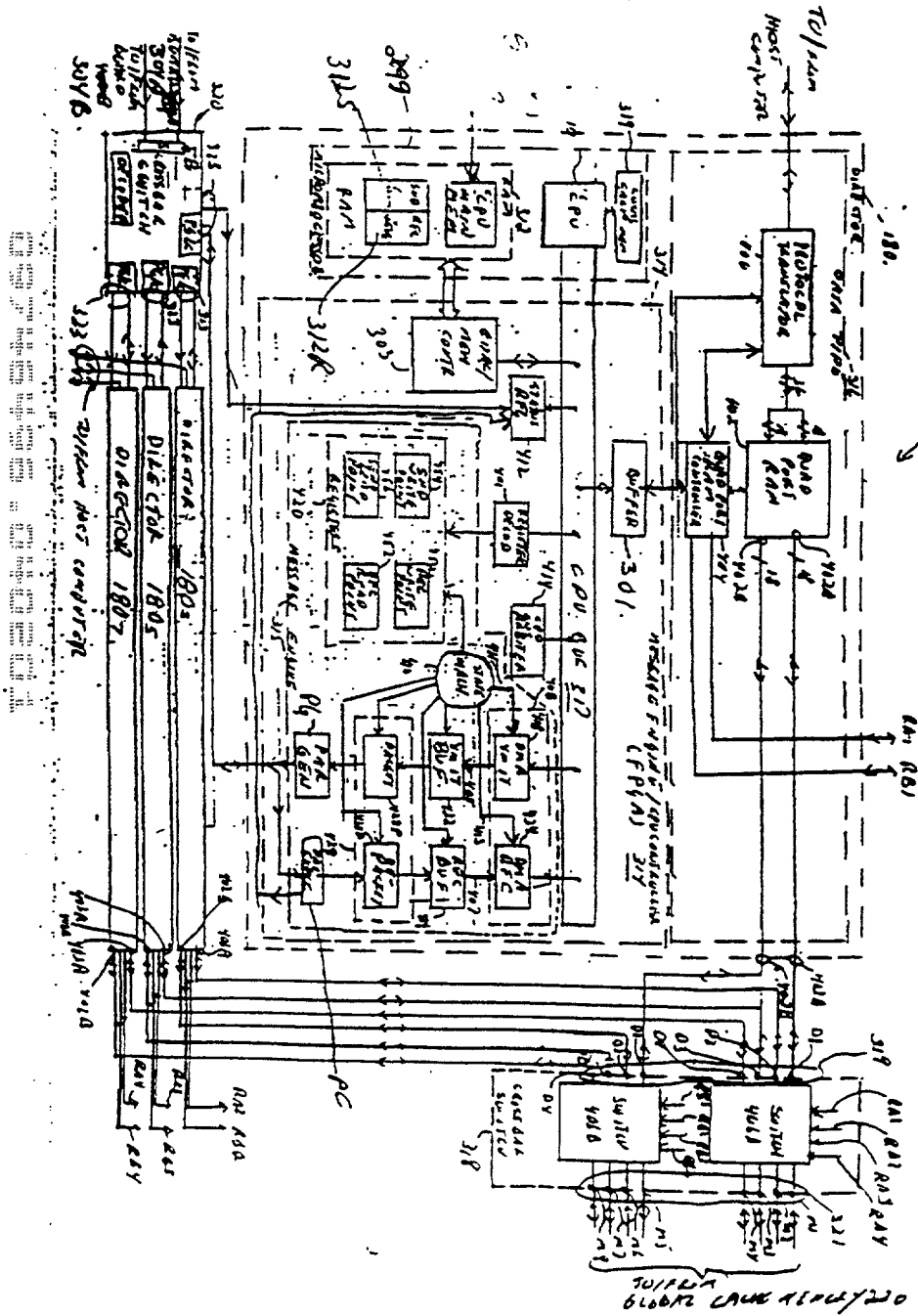
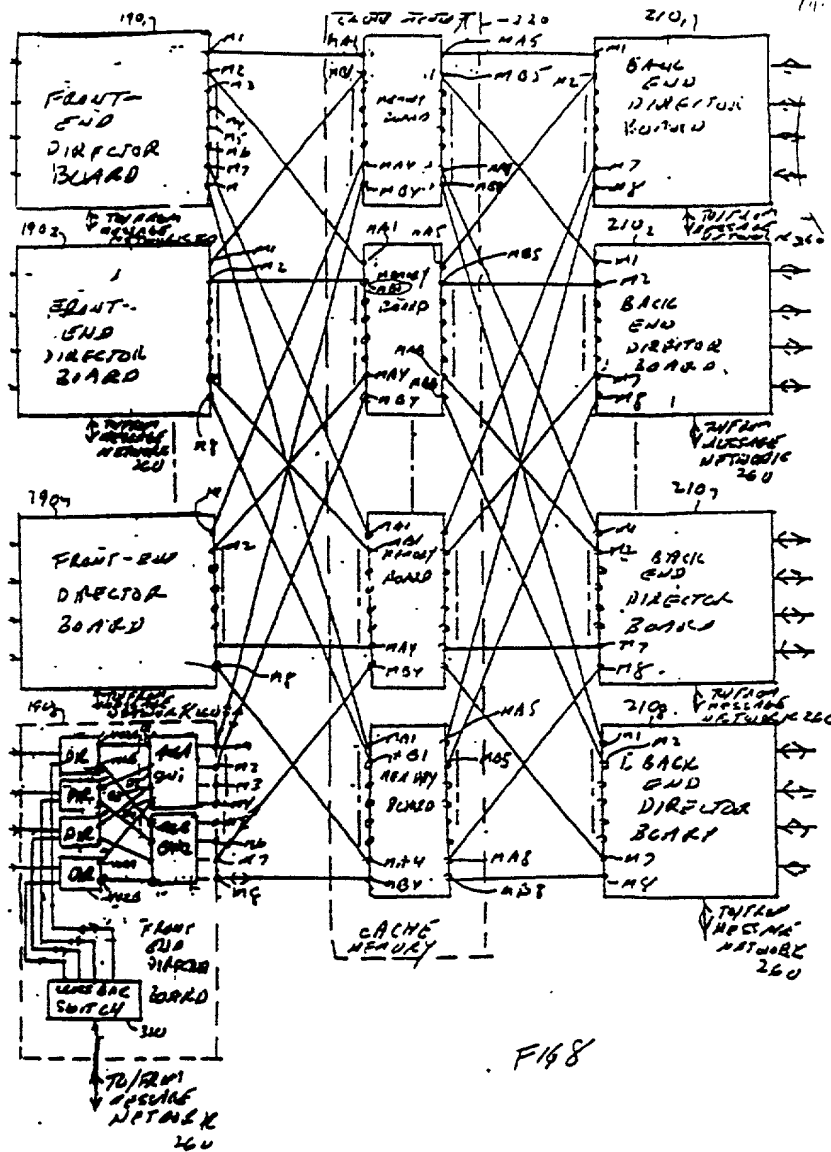
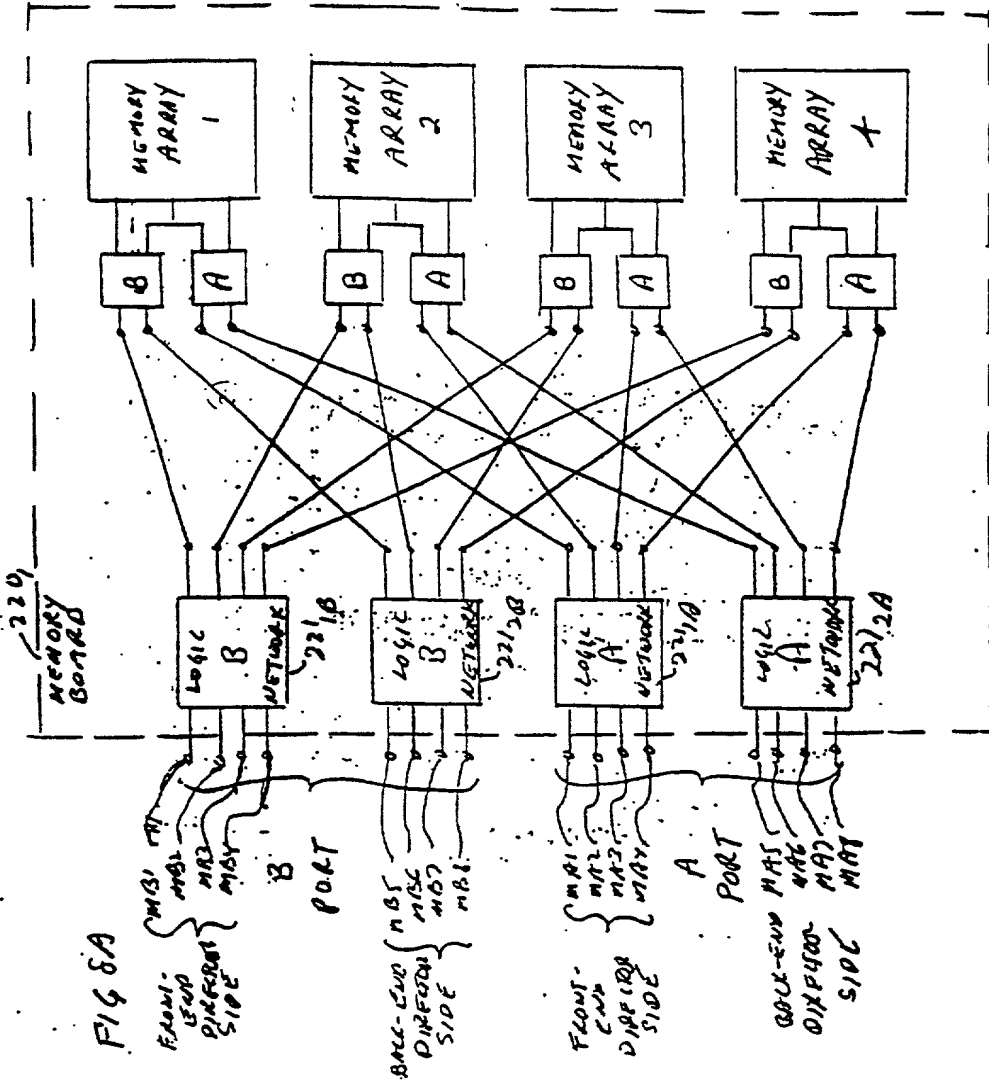


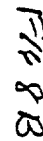
FIG. 7 MEMORY BOARD 180

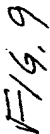




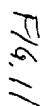








The diagram illustrates a 4x4 crossbar switch architecture. It consists of four input sections (A, B, C, D) and four output sections (W, X, Y, Z). Each section is divided into an upper and lower port-interface section. The input sections are connected to the output sections via a central grid of crosspoints. The diagram includes various control signals such as 'CLOCK', 'CLOCK1', 'CLOCK2', 'CLOCK3', and 'CLOCK4'. The overall structure is labeled 'CROSS BAR 5004' and 'FIG. 10'.



WORK PORT  
INTERFACE  
SECTION

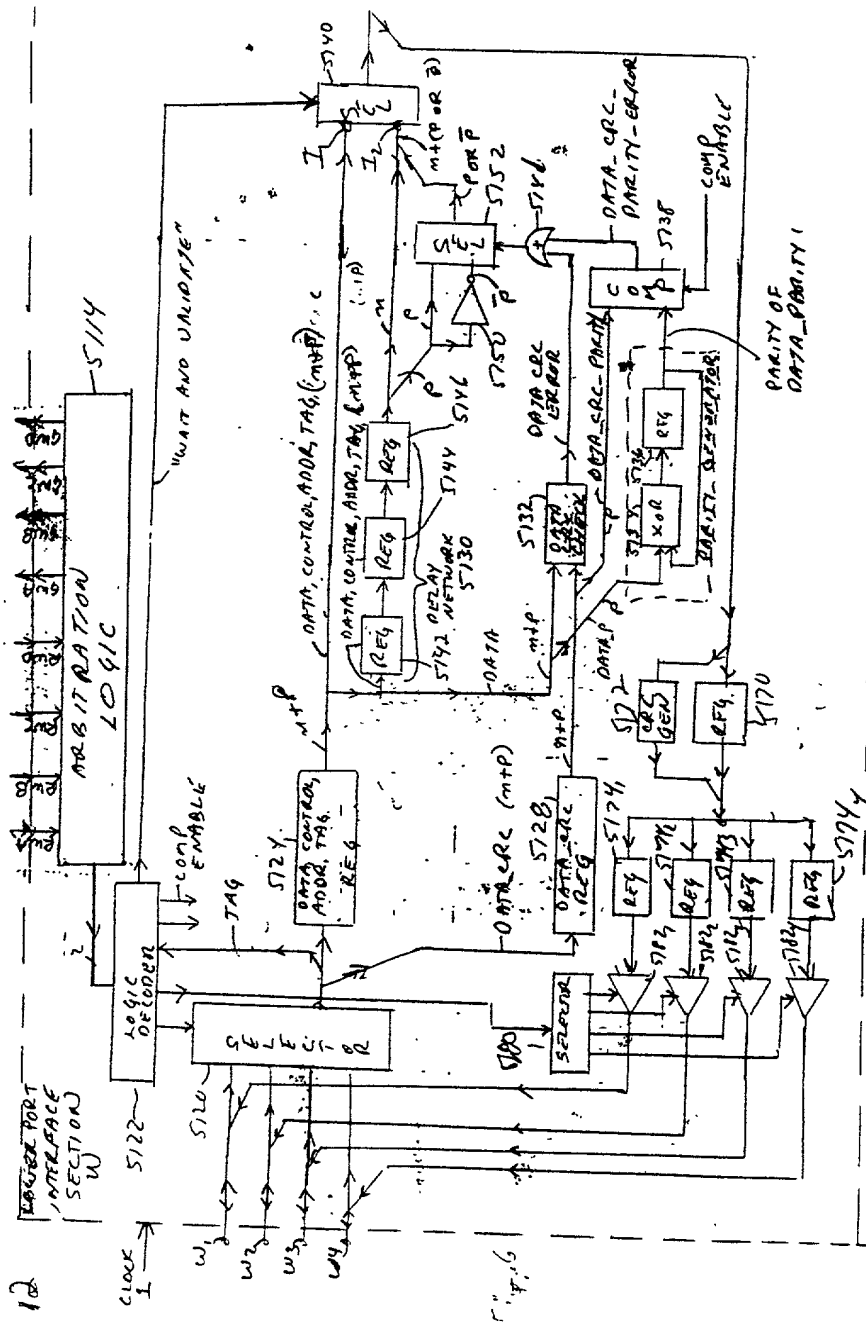
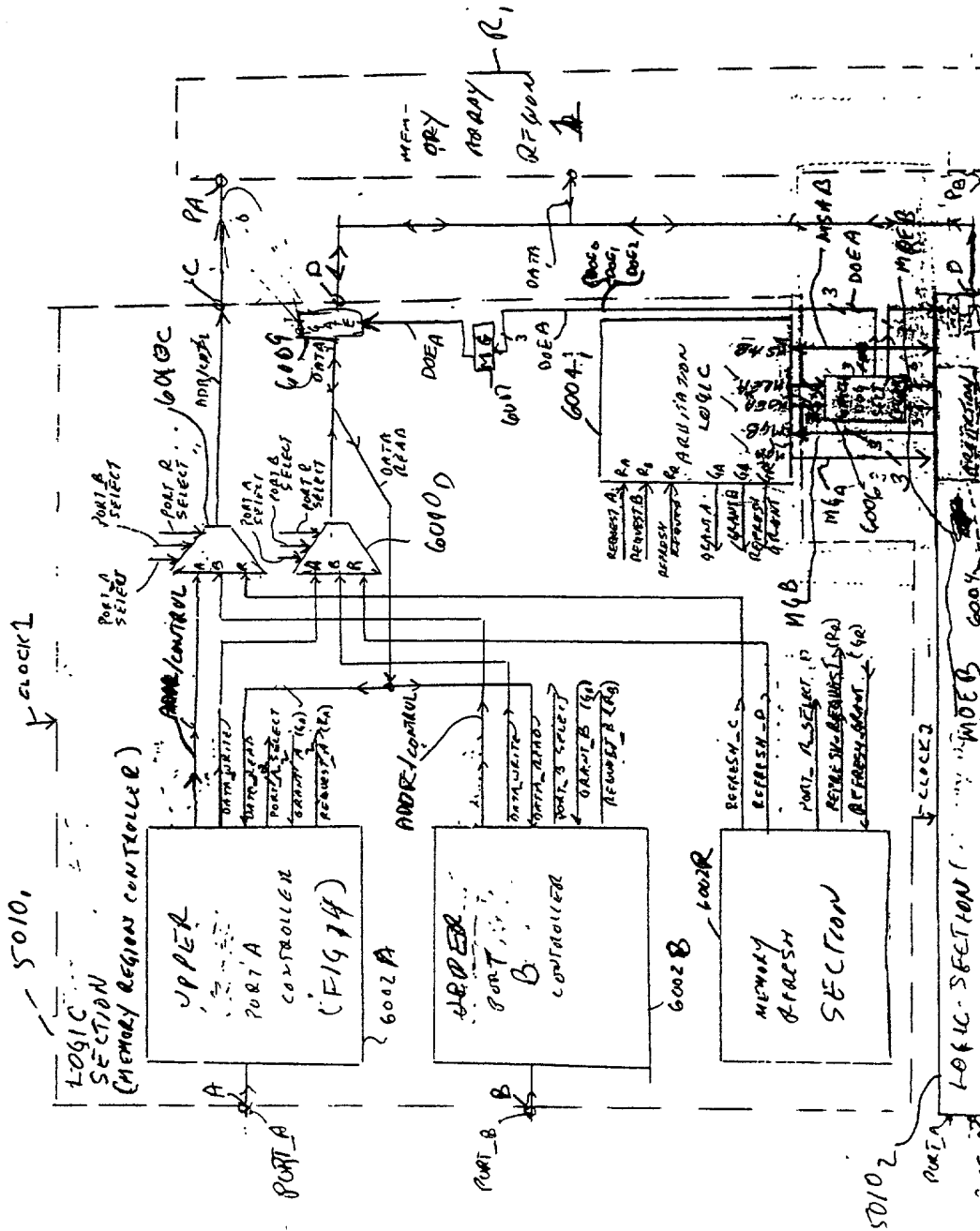
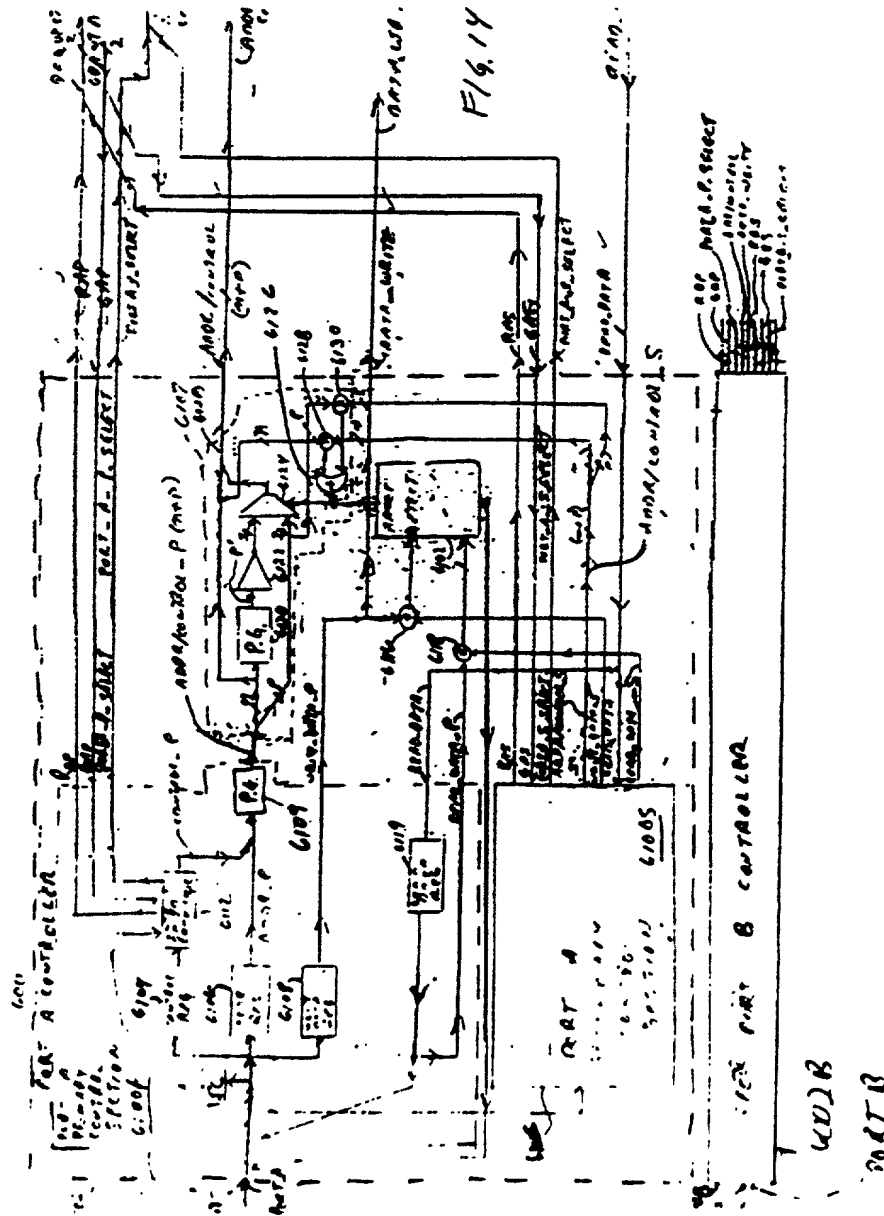
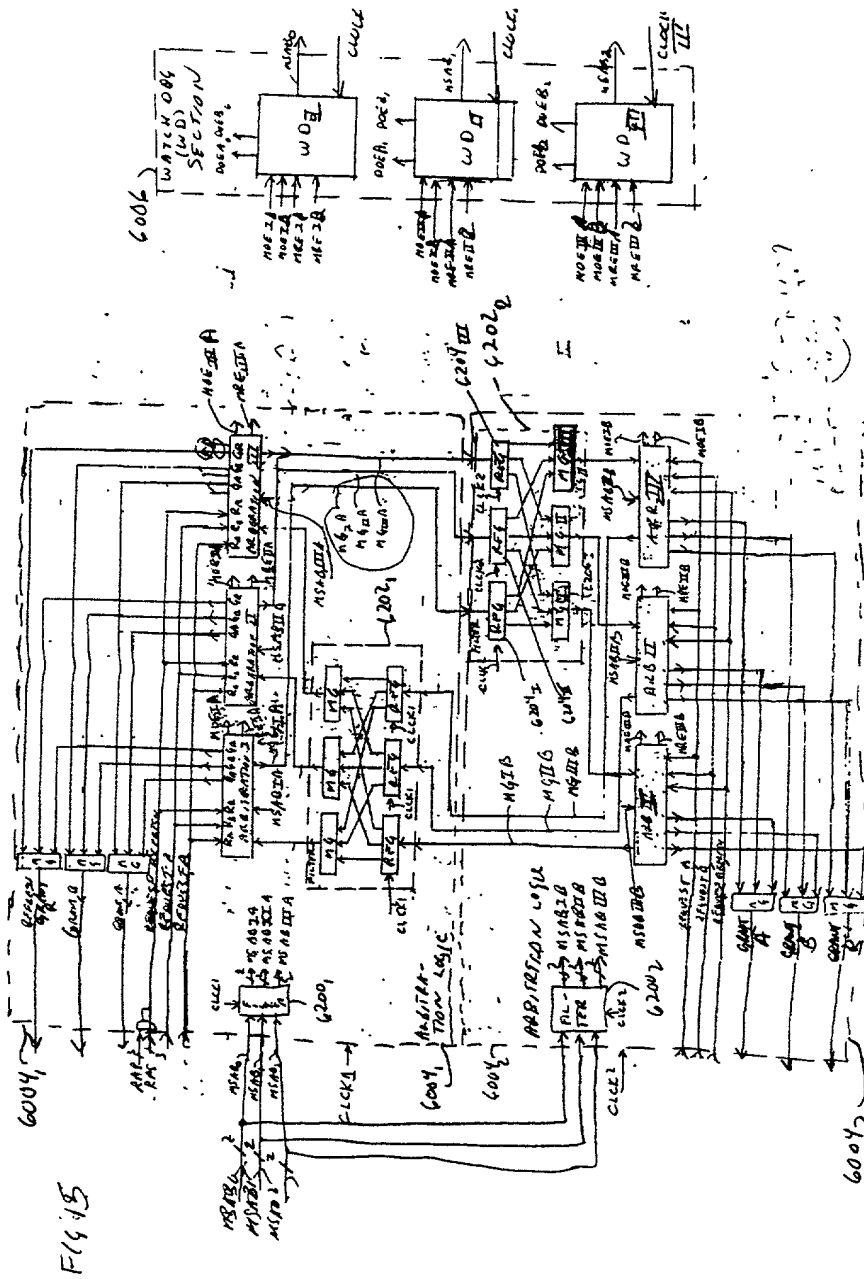


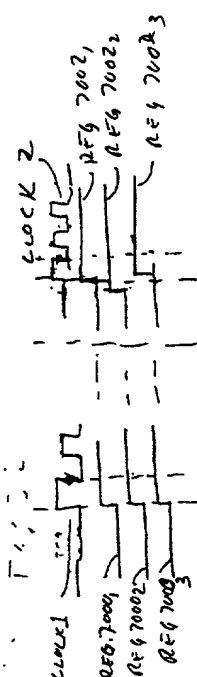
FIG. 1B



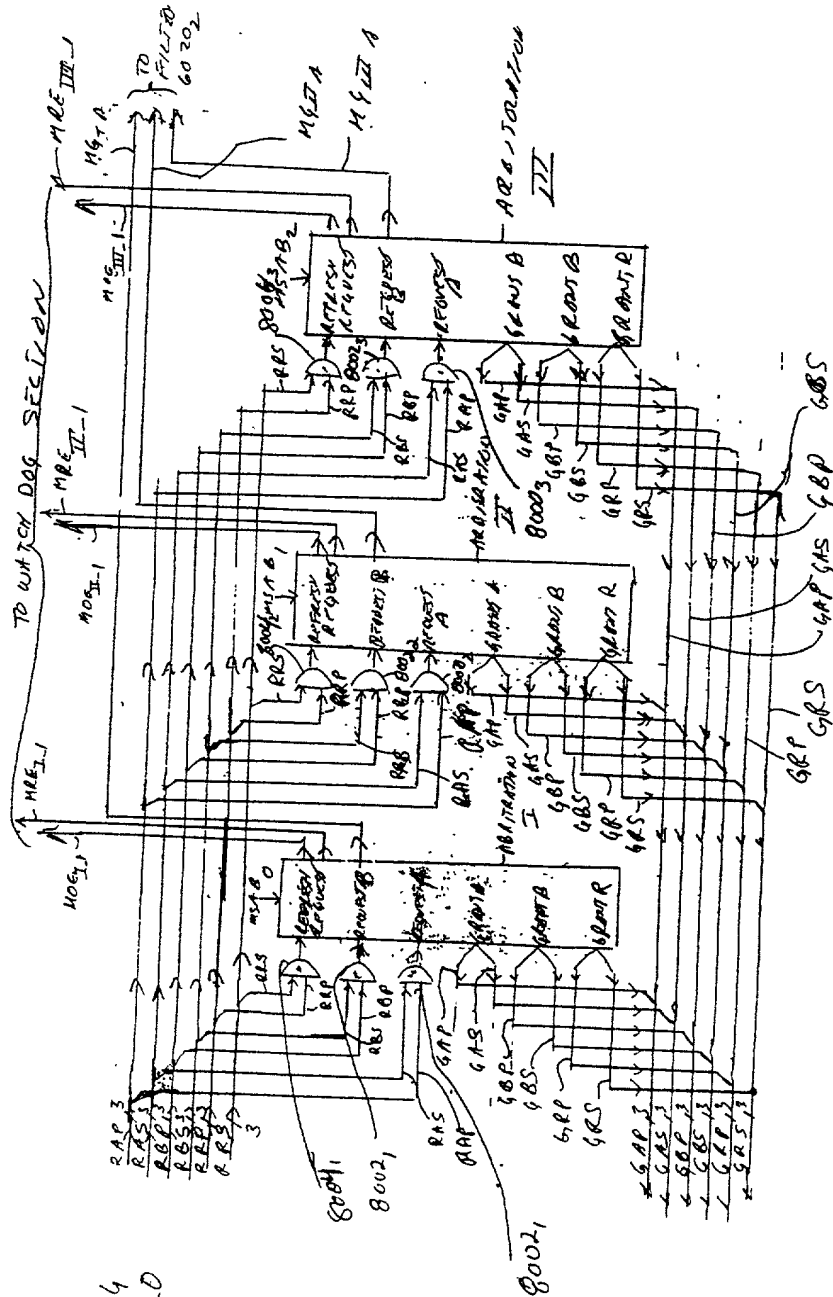








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